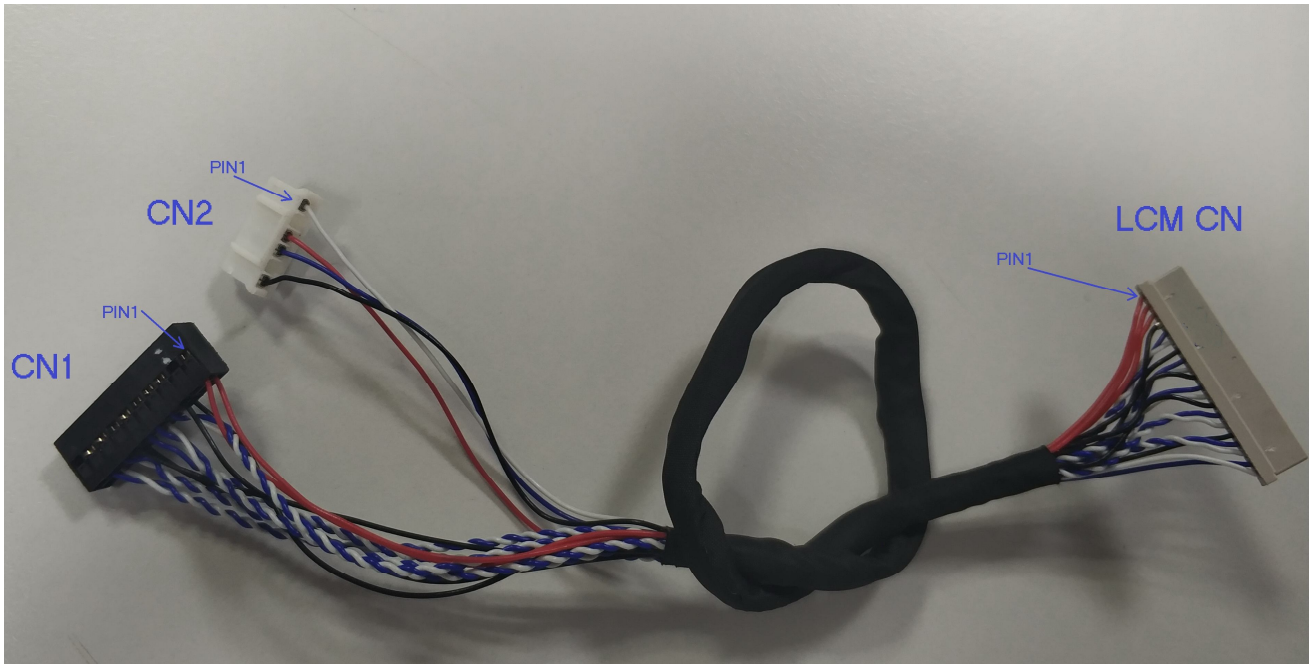


## 杜邦线接口定义



杜邦线长250mm(不含CONNECTOR)

### 1、Board CN1

Pin No.	Symbol	I/O	Function	Remark
1	VCC	P	Power supply VDDIN=3.3V	
2	VCC	P	Power supply VDDIN=3.3V	
3	NC	-	No Connection	
4	NC	-	No Connection	
5	GND	P	Ground	
6	GND	P	Ground	
7	RXIN0N	I	-LVDS differential data	
8	RXIN0P	I	+LVDS differential data	
9	RXIN1N	I	-LVDS differential data	
10	RXIN1P	I	+LVDS differential data	
11	RXIN2N	I	-LVDS differential data	
12	RXIN2P	I	+LVDS differential data	
13	GND	P	Ground	
14	GND	P	Ground	
15	RX_CLKN	I	-LVDS differential clock input	
16	RX_CLKP	I	+LVDS differential clock input	
17	RXIN3N	I	-LVDS differential data	
18	RXIN3P	I	+LVDS differential data	
19	NC	-	No Connection	
20	NC	-	No Connection	

## 2、 Board CN2 (Backlight)

Pin No.	Symbol	I/O	Function	Remark
1	VLED	P	Power supply VLED=5~12V(Typ.)	
2	NC	-	No Connection	
3	LED_EN	I	Backlight Enable Signal,3.3V~5V.	
4	PWM	I	Backlight brightness:apply 0.7V to 1.4V DC voltage signal	
5	NC	-	No Connection	
6	GND	P	Ground	

## 3、 LCM CN

Pin No.	Symbol	I/O	Function	Remark
1	VDDIN	P	Power supply VDDIN=3.3V	
2	VDDIN	P	Power supply VDDIN=3.3V	
3	LED_EN	I	Backlight Enable Signal,3.3V~5V.	
4	GND	P	Ground	
5	RXIN0N	I	-LVDS differential data	
6	RXIN0P	I	+LVDS differential data	
7	GND	P	Ground	
8	RXIN1N	I	-LVDS differential data	
9	RXIN1P	I	+LVDS differential data	
10	GND	P	Ground	
11	RXIN2-	I	-LVDS differential data	
12	RXIN2+	I	+LVDS differential data	
13	GND	P	Ground	
14	RX_CLKN	I	-LVDS differential clock input	
15	RX_CLKP	I	+LVDS differential clock input	
16	GND	P	Ground	
17	RXIN3-	I	-LVDS differential data	
18	RXIN3+	I	+LVDS differential data	
19	PWM	I	Backlight brightness:apply 0.7V to 1.4V DC voltage signal	
20	VLED	P	Power supply VLED=5~12V(Typ.)	

Note: I/O definition:

I: input, O: output, P: Power,

-: No Connection